

FIG. 2

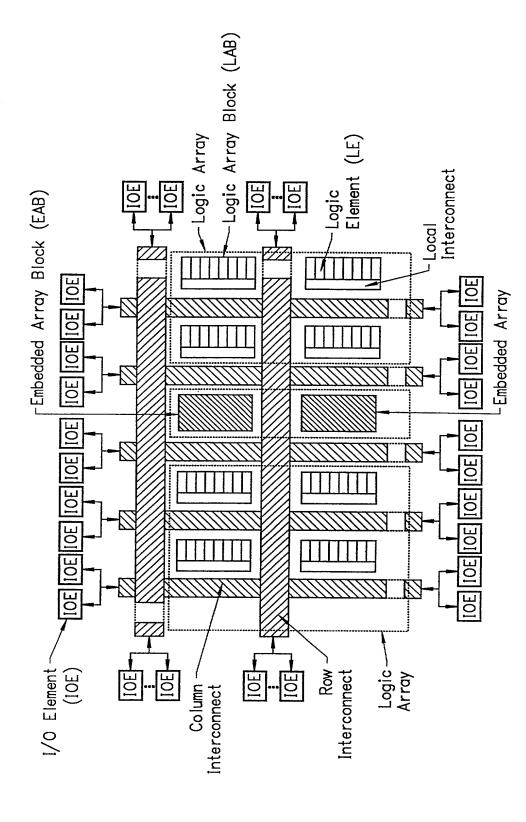


FIG. 4

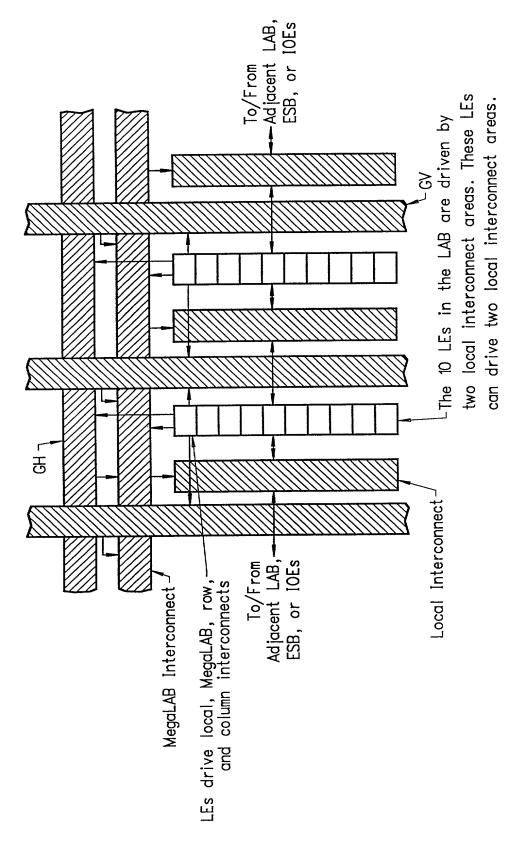
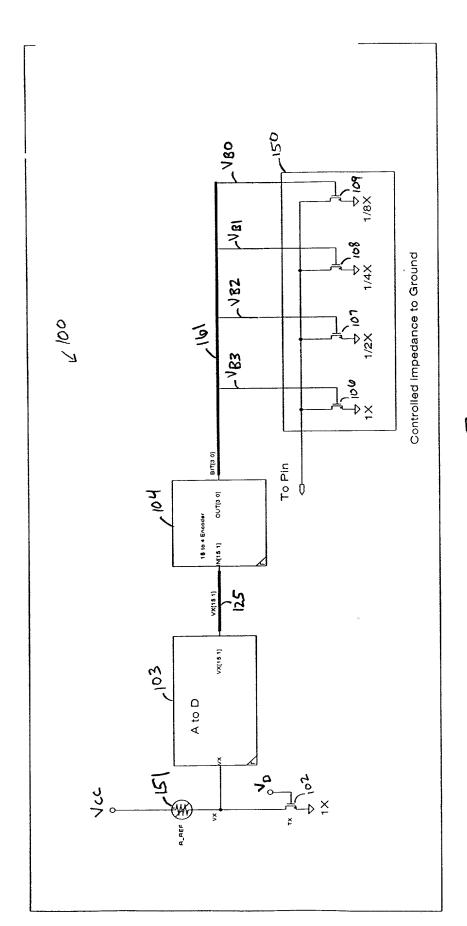
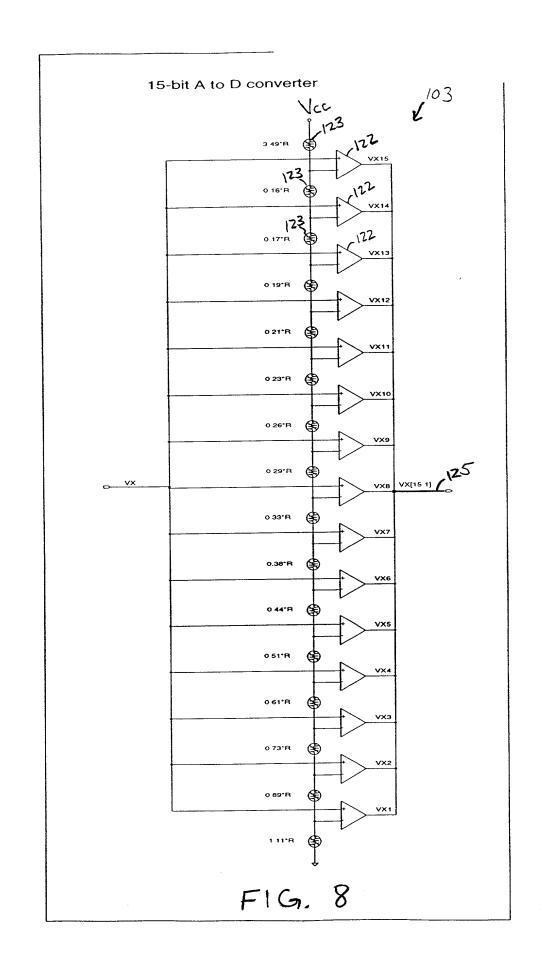


FIG. 5

## Represents on-chip termination FIG. 6A F16. 6B 50 F16.6C



F1G, 7



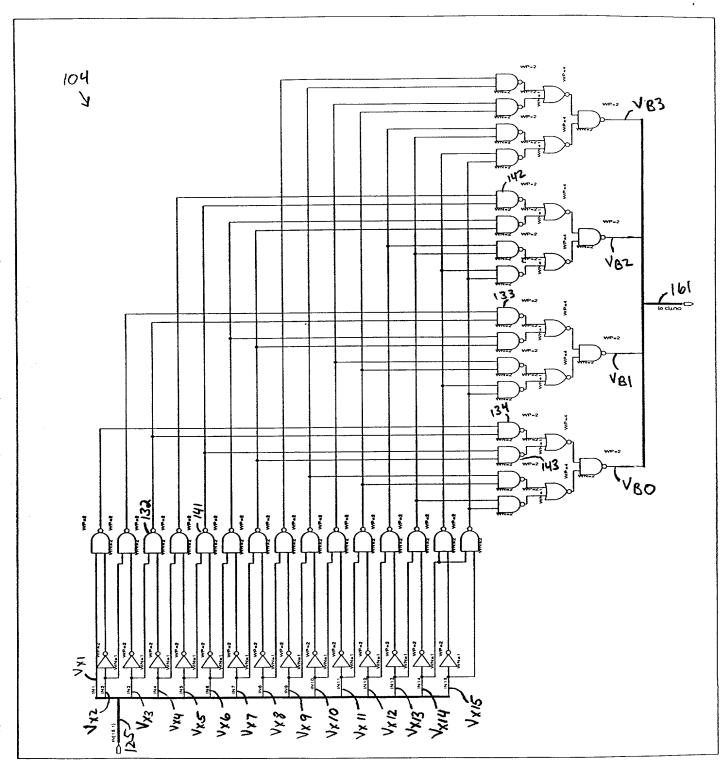


FIG. 9